



WELCOME TO
E-Link



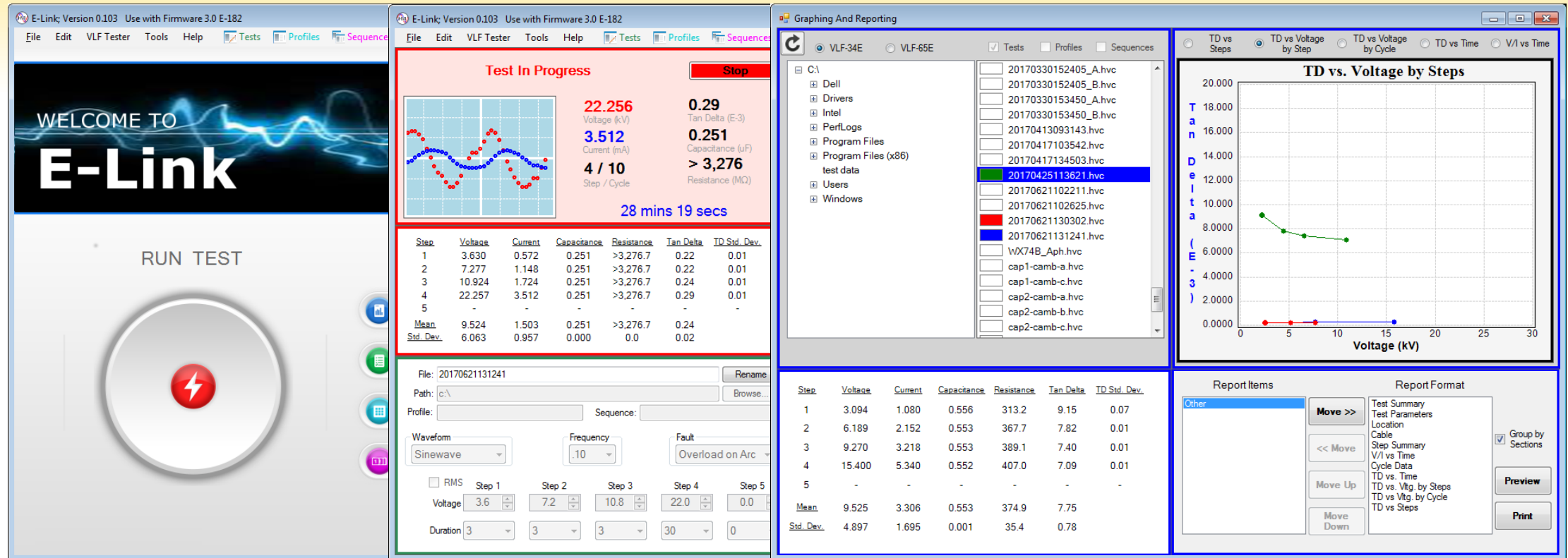
ISO 9001 2015

The World's Source for High Voltage Test Equipment

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E-Link is HVI's feature packed remote control and report generation software package



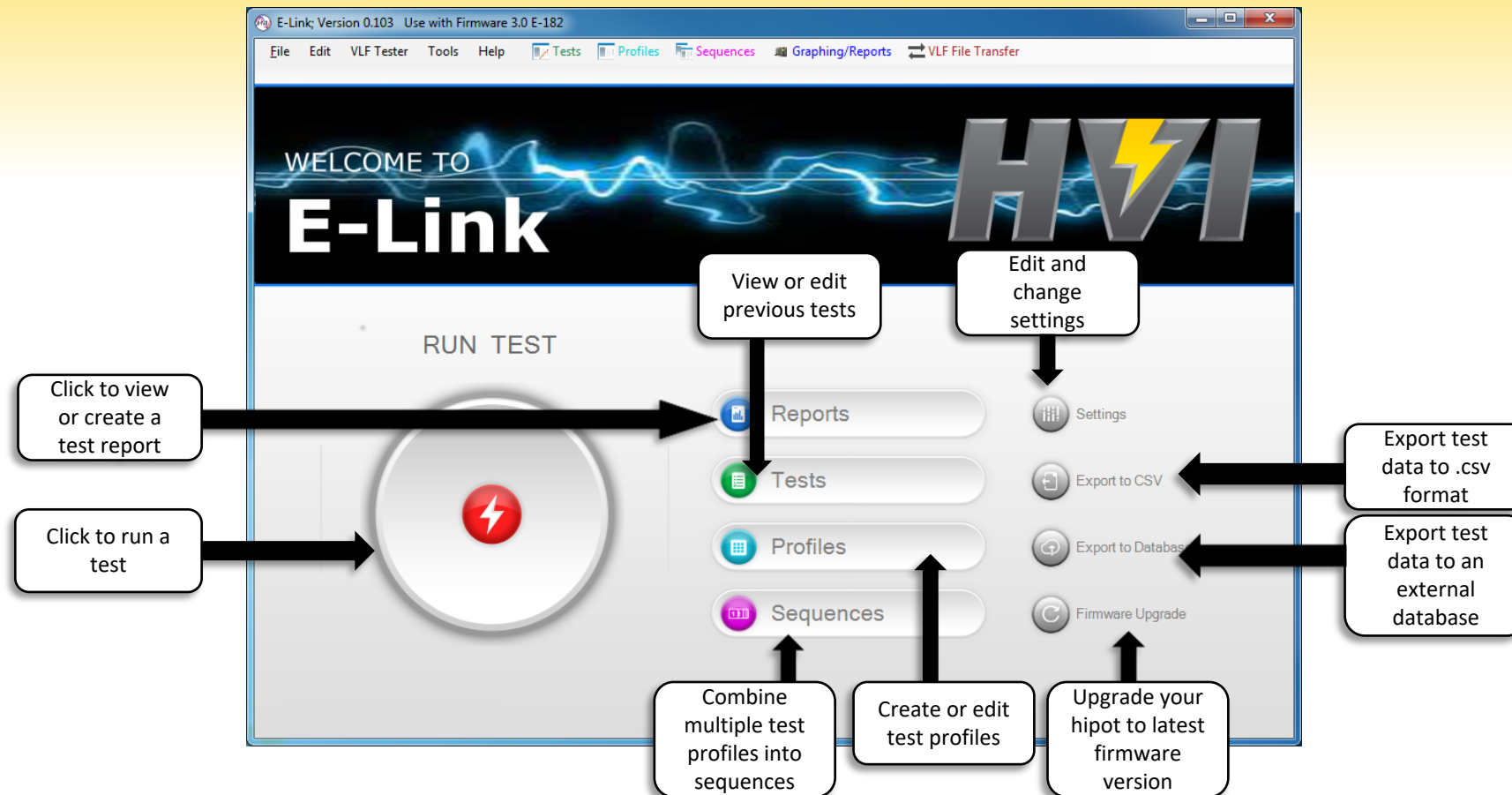
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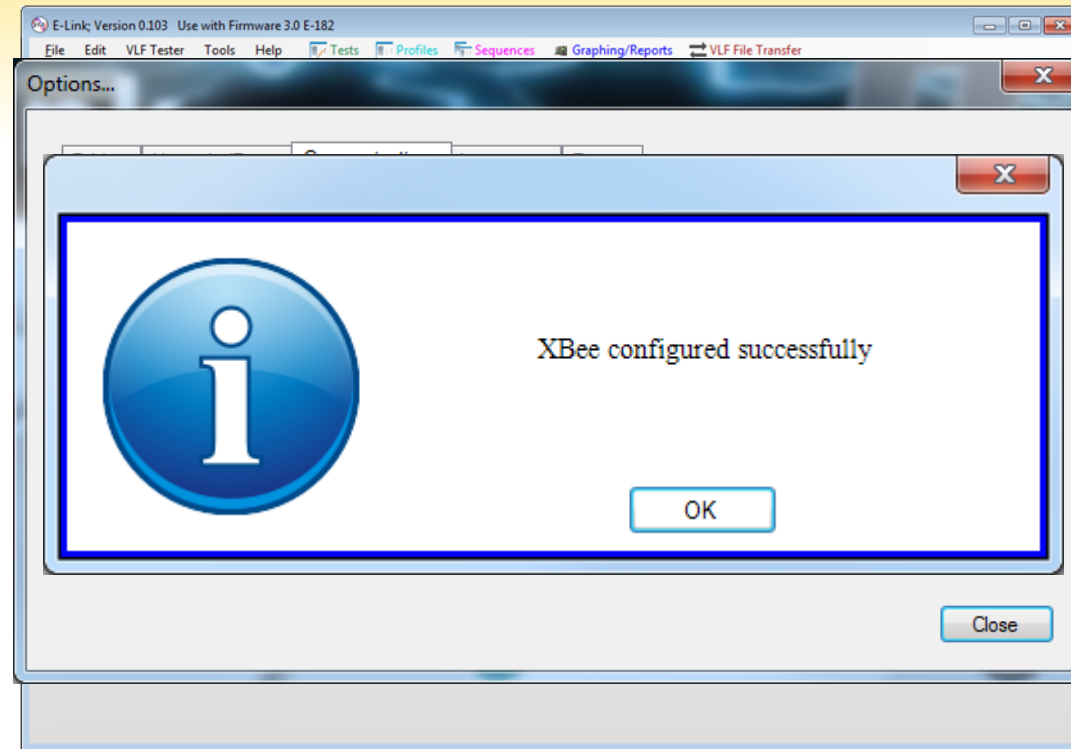
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The Home Screen



Connecting Wirelessly to VLF



Remote Operation of Hipot via XBee Wireless Protocol

The image displays three sequential screenshots of the E-Link software interface, illustrating the workflow for remote operation of Hipot via XBee wireless protocol. The interface is titled "E-Link: Version 0.103 Use with Firmware 3.0 E-182".

Screenshot 1 (Left): The "WELCOME E-LINK" screen. A callout box says "Click Run Test". Below the welcome message, there is a large "RUN TEST" button. Another callout box says "Choose waveform: + DC, - DC, sinewave or squarewave". A third callout box says "Click this box to toggle between RMS and Peak metering, metering in RMS if checked".

Screenshot 2 (Middle): The "Test Configuration" screen. A callout box says "Click Start for a second time to confirm action". The screen shows various test parameters: Voltage (kV), Current (mA), Step / Cycle, Tan Delta (E-3), Capacitance (uF), and Resistance (MQ). The "Measure Source" section has buttons for VLF, VLF/TD, TD, and SIM. The "Signal Strength" section shows a bar graph. The "VLF Model" is set to "HIGH VOLTAGE INC. VLF-34E". The "VLF Date and Time" is "Wednesday Jun 21, 2017 02:47". The "Waveform" is set to "Sinewave". The "Frequency" is "10". The "Fault" is set to "Overload on Arc". The "RMS" checkbox is checked. The "Step 1" through "Step 5" sections show "Voltage" and "Duration" settings.

Screenshot 3 (Right): The "Test In Progress" screen. A callout box says "Graph for analysis of TD, V, I, and Time". The screen shows a graph of "TD vs. Steps" with a peak value of 22.256 and a time of 28 mins 19 secs. A table of test data is displayed below the graph. A callout box says "Live test data". Another callout box says "Test data from each step". A third callout box says "Add identifying information about the system under test". The table of test data is as follows:

Step	Voltage	Current	Capacitance	Resistance	Tan Delta	TD vs. Step
1	3.630	0.572	0.251	>3.276.7	0.22	0.01
2	7.277	1.148	0.251	>3.276.7	0.22	0.01
3	10.924	1.724	0.251	>3.276.7	0.24	0.01
4	22.257	3.512	0.251	>3.276.7	0.29	0.01
5						
Mean	9.524	1.503	0.251	>3.276.7	0.24	
Std. Dev.	6.063	0.957	0.000	0.0	0.02	

The "TD vs. Steps" graph shows a peak value of 22.256 and a time of 28 mins 19 secs. The "Test data from each step" table shows the test results for each step. The "Add identifying information about the system under test" section includes fields for Circuit ID, Phase, Location, Project, Building, Sub-Station, Splices, Length, Comment, Manufacturer, Insulation, Cable Rating, Conductor, Termination, Environment, Tester, and Witness.

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Custom Report Generation

Choose the location where the test data is stored

Choose files for the report

Choose what data for the report

Your test report has been created

Test Demonstration

Test Summary

File Name	Total Steps	Total Cycles	Total Duration	Tan Delta (E-3)	Capacitance (uF)	Resistance (MOhm)
20170621131241.hvc	4	68	00:11:20	0.517	0.250	> 3,276 M
20170621130302.hvc	3	54	00:09:00	0.337	0.251	> 3,276 M
20170425113621.hvc	4	84	00:14:00	8.115	0.552	374.904

Parameters - 20170621131241.hvc

Waveform: Sinewave
File Name: c:\20170621131241.hvc
Date/Time: 6/21/2017 1:19:18 PM

Frequency: .1 (hz)
Phase: A
Fault: Overload

Location - 20170621131241.hvc

Circuit ID:
Sub-Station:
Location:
Building:
Project:

Location - 20170621130302.hvc

Circuit ID:
Sub-Station:
Location:
Building:
Project:

Step	Voltage	Current	Capacitance	Resistance	Tan Delta	TD Std. Dev.
1	3.094	1.080	0.556	313.2	9.15	0.07
2	6.189	2.152	0.557	313.2	9.15	0.07
3	9.270	3.218	0.558	313.2	9.15	0.07
4	15.400	5.340	0.559	313.2	9.15	0.07
5	-	-	-	-	-	-
Mean	9.525	3.306	0.553	374.9	7.75	-
Std. Dev.	4.897	1.695	0.001	35.4	0.78	-

Change the Test Report Title

The screenshot shows the E-Link software interface. The main window displays 'WELCOME TO E-Link' and a 'RUN TEST' button. An 'Options...' dialog box is open, showing the 'Folders' tab with the title '15kV VLF Withstand Test' and a 'Logo file' field. To the right, a 'Print Preview' window shows the test report for '15kV VLF Withstand Test'. The report includes a 'Test Summary' table, 'Parameters', 'Location', and 'Cable' details. A callout box points to the title '15kV VLF Withstand Test' in the report.

Options...

Folders Upgrade/Export Communications Languages

15kV VLF Withstand Test

Title

Logo file

Print Preview

The test report has an updated title

15kV VLF Withstand Test

Test Summary

File Name	Total Steps	Total Cycles	Total Duration	Tan Delta (E-3)	Capacitance (uF)	Resistance (MOhm)
20190509140629.hvc	3	54	00:09:00	7.648	0.559	382.308

Parameters - 20190509140629.hvc

Waveform: Sinewave
File Name: C:\20190509140629.hvc
Date/Time: 5/9/2019 2:06:31 PM

Frequency: .1 (hz)
Phase: A
Fault: Overload

Location - 20190509140629.hvc

Circuit ID: demo
Sub-Station: NA

Location: hvi
Building: High Bay
Project: TD Demo

Cable - 20190509140629.hvc

Cable Rating: 80kV
Conductor: Copper
Insulation: Oil

Add Your Company's Logo to Report

The image shows two windows from the E-Link software. The main window on the left is titled 'E-Link: Version 0.103 Use with Firmware 3.0 E-182' and features a 'WELCOME TO E-Link' banner and a 'RUN TEST' button with a red lightning bolt icon. An 'Options...' dialog box is open, with the 'Reports' tab selected. It contains a 'Test Demonstration' section with a 'Title' field and a 'Logo file' field. The 'Print Preview' window on the right shows a test report titled 'Test Demonstration' with the HVI VLF-34E logo in the top right corner. A callout box points to the logo area with the text 'Your company logo will appear here'. The report includes a 'Test Summary' table, 'Parameters' for a 20170621131241.hvc file, and 'Location' information for two different test files.

Options... Reports

Test Demonstration

Title

Logo file

Print Preview

Your company logo will appear here

Test Demonstration

Test Summary

File Name	Total Steps	Total Cycles	Total Duration	Tan Delta (E-3)	Capacitance (uF)	Resistance (MOhm)
20170621131241.hvc	4	68	00:11:20	0.517	0.250	> 3,276 M
20170621130302.hvc	3	54	00:09:00	0.337	0.251	> 3,276 M
20170425113621.hvc	4	84	00:14:00	8.115	0.552	374.904

Parameters - 20170621131241.hvc

Waveform: Sinewave Frequency: .1 (hz) Fault: Overload

File Name: c:\20170621131241.hvc Phase: A

Date/Time: 6/21/2017 1:19:18 PM

Location - 20170621131241.hvc

Circuit ID: Location: Project:

Sub-Station: Building:

Location - 20170621130302.hvc

Circuit ID: Location: Project:

Sub-Station: Building:

Export to .csv for Advanced Report Generation

WELCOME TO
E-Link

RUN TEST

Choose one of the saved test data files which are .hvc files

Your test report data has been imported to .csv format, open with Excel or other spreadsheet program

Sequence	Profile	File Name	Date/Time	VLF Mode	Waveform	Frequency	Fault	Phase	Location	Circuit ID	Test Name	Facility	Building
1		201706211	#####	34E	Sinewave	0.1			0			'2017062111342'	

Step	Cycles	Source Vo	Duration	Voltage	F Current	P Tan Delta	Capacitan	Resistanc	Event
1	1	22	30	22.119	3.232	-	0.233	6.844	
1	2	22	30	22.142	3.529	-	0.254	6.274	
1	3	22	30	22.141	3.53	-	0.254	6.272	
1	4	22	30	22.141	3.529	-	0.254	6.274	
1	5	22	30	22.141	3.53	-	0.254	6.272	
1	6	22	30	22.141	3.529	-	0.254	6.274	
1	7	22	30	22.141	3.529	-	0.254	6.274	
1	8	22	30	22.141	3.53	-	0.254	6.272	

Build Custom Test Profiles

The image shows three overlapping windows from the E-Link software. The leftmost window is the main interface with a 'RUN TEST' button. The middle window is titled 'New/Edit Profile...' and shows a file explorer with 'Test Demo.hvt' selected. A callout box points to the 'Test Demo' text, saying 'Create a profile name and press ok'. The rightmost window is also titled 'New/Edit Profile...' and shows configuration options. A callout box points to the waveform, frequency, and overload settings, saying 'Choose your waveform, frequency, overload reaction, add test voltage and dwell time for each voltage for up to 5 steps'. Another callout box points to the system identification fields, saying 'Add test location and system identification information here'.

WELCOME TO
E-Link

RUN TEST

Click

Test Demo.hvt

Test Demonstration.hvt

Test Demonstration

Enter profile name

Create a profile name and press ok

create test profile

Choose your waveform, frequency, overload reaction, add test voltage and dwell time for each voltage for up to 5 steps

Add test location and system identification information here

File: Test Demonstration

Path: C:\

Profile: Test Demonstration

Sequence:

Waveform: Sinewave

Frequency: 10

Fault: Overload on Arc

☒ RMS

Step 1 Step 2 Step 3 Step 4 Step 5

Voltage: 4.0 8.0 12.0 16.0 0.0

Duration: 2 3 3 30 0

Circuit ID

Manufacturer

Phase

Insulation

Location

Cable Rating

Project

Conductor

Building

Termination

Sub-Station

Environment

Splices

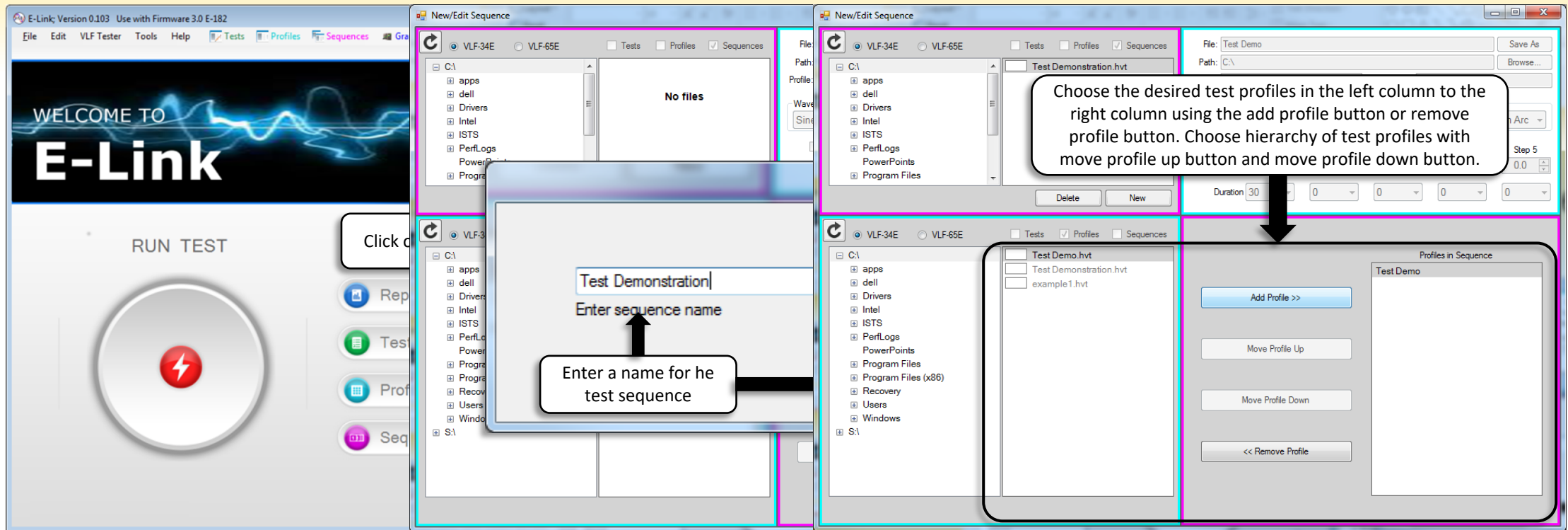
Tester

Length

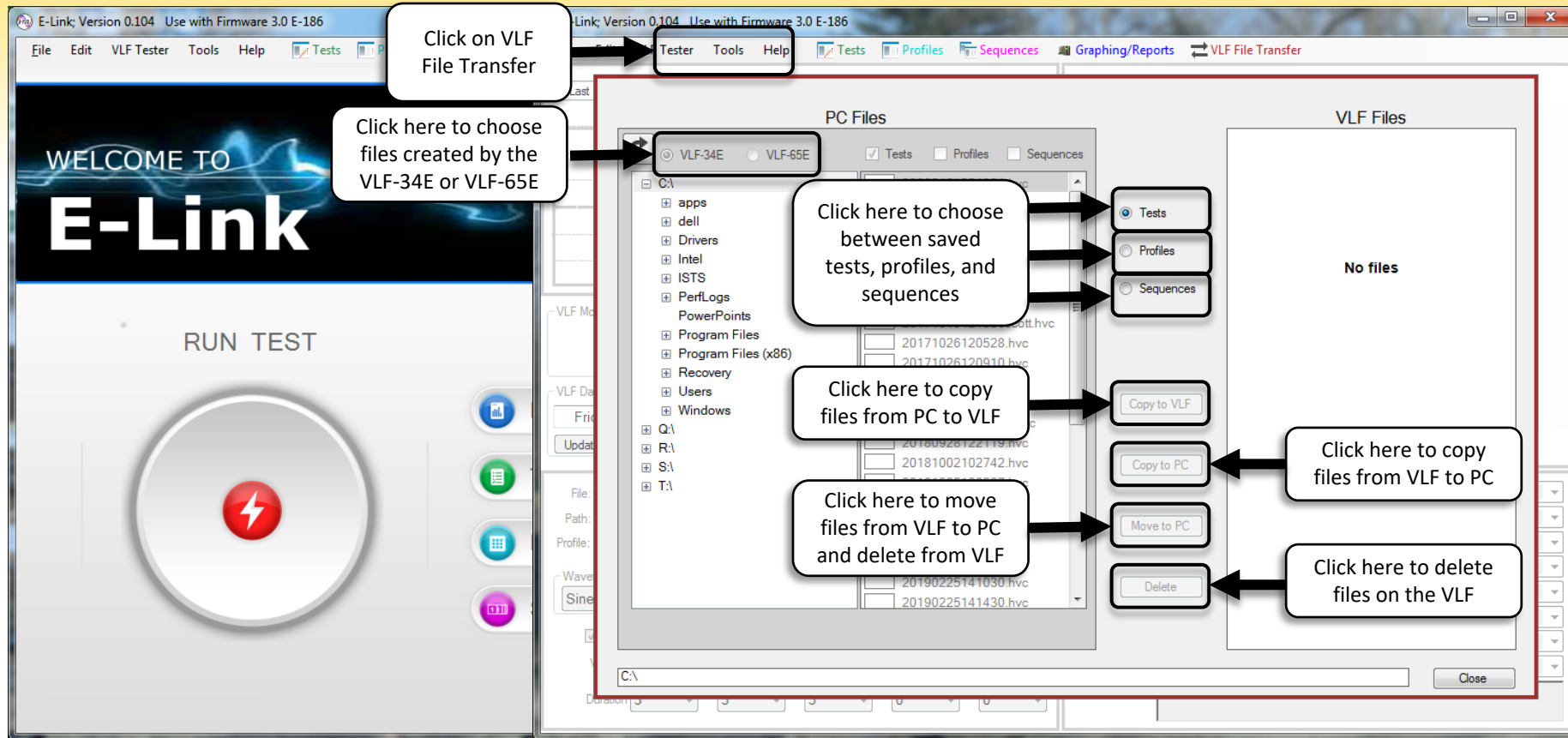
Witness

Comment

Build Custom Test Sequences



Wireless File Transfer Between PC and VLF



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Simulation Mode

The image displays three sequential screenshots of the E-Link software interface, illustrating the steps to enter and use Simulation Mode. Callouts provide instructions for each step:

- First Screenshot:** The 'RUN TEST' button is highlighted with the callout: "Click run test". Below it, the 'Waveform' section shows 'Sinewave' selected, with a callout: "Choose waveform: + DC, - DC, sinewave or squarewave". The 'RMS' checkbox is also highlighted with the callout: "Click this box to toggle between RMS and Peak metering, metering in RMS if checked".
- Second Screenshot:** The 'Measure Source' section shows 'SIM' (Simulation) selected, with a callout: "Choose sim as measurement source". The 'VLF Model' is set to 'HIGH VOLTAGE INC. VLF-34E'. The 'VLF Date and Time' is set to 'Friday Jan 01, 2010 12:00'. The 'Signal Strength' graph is visible. Below, the 'Steps' table is shown with callouts: "Choose a voltage and dwell time to spend at each voltage" pointing to the 'Voltage' and 'Duration' columns.
- Third Screenshot:** The 'Test In Progress' window is shown, displaying a graph of simulated TD, V, I, and time data. The 'Simulation' window is open, showing a table of simulated test data. Callouts indicate: "Click start twice to confirm action" pointing to the 'Start' button, "Graph for analysis of simulated TD, V, I, and time data" pointing to the graph, "Simulated test data" pointing to the 'Simulated test data' table, and "Simulated test data from each step" pointing to the 'Simulated test data from each step' table.

Step	V	I	TD	Std. Dev.
1	0.000	0.000	0.000	0.00
2	0.000	0.000	0.000	0.00
3	0.000	0.000	0.000	0.00
4	0.000	0.000	0.000	0.00
5	0.000	0.000	0.000	0.00

Thank You

from



Thank you for watching, if you have any questions please contact High Voltage, Inc. or your local High Voltage, Inc. sales representative



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